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L15 15 and 16
105 L15
L14 L13 and 19
6 L14
L13 (712/225)[CCLS]
499 L13
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L12 L9 and 18
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L10 L9 and 16
90 L10
L9 15 and decod\$6
208 L9
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<u>L8</u>	(709/236)[CCLS]	801	<u>L8</u>
<u>L7</u>	(370/389)[CCLS]	2626	<u>L7</u>
<u>L6</u>	(712/2-300)[CCLS]	11036	<u>L6</u>
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(parallel\$5 or concurrent\$4 or simultaneous\$4) near5 (alu\$1 or execut\$4)			
<u>L5</u>	near45 (different\$4 or variable or var\$5 or lower or slower or faster or higher or greater) near8 (speed\$3 or rate\$1 or bandwidth\$1)	462	<u>L5</u>
<i>DB=USPT; PLUR=YES; OP=OR</i>			
<u>L4</u>	L3 and (concurrent or simultaneous\$5)	1	<u>L4</u>
<u>L3</u>	L2 and execut\$6	1	<u>L3</u>
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IEEE JNL IEEE Journal or Magazine

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IEEE CNF IEEE Conference Proceeding

IEEE STD IEEE Standard

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Citation & Abstract

**1. PC-based DTV architecture**Iran, T.; Moriarty, M.; Giang Dao; Welker, M.;
Consumer Electronics, IEEE Transactions on
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